## GBCS SCHEME

USN						000		17CS44
Marine .	entiel h	Fourth	Semeste	r R.E. De	oree Eval	mination D	ec 2023/Jan	2024
Fourth Semester B.E. Degree Examination, Dec.2023/Jan.2024  Microprocessor & Microcontrollers								
Tim	/	2 1	MICIC	proces	SOF & IV	ilcrocont		N/ 1 100
1 1111	ie: .	3 hrs.				<b>Y</b>	Max.	Marks: 100
	N	ote: Answ	ver any FIV	E full quest	ions, choosin	g ONE full que	estion from each	module.
							4	
			1124		Module-		4	
1	a.		Microproces	sors. With r	leat diagram	recall the vario	ous registers of B	
	1	8086.	1.				1: 0000	(07 Marks)
						egisters are use		(07 Marks)
	c.	write an	Assembly	Language Pr	ogram (ALP)	to find the fibo	onacci series of ter	n numbers. (06 Marks)
						Z.		(00 Marks)
			( )		OR			
2	a.	Explain	the various	addressing m		with example.		(07 Marks)
	b.						, DI = 0850, BI	
							act physical men	
					e following ca			
		(i)	MOV[B2	X] + 20, AX			7	
		(ii)	MOV[Si]	+ 10, AX	X.			
		(iii)		] + 4, AX	·		24	
		(iv)	-	]+12, AX				(08 Marks)
	c.	Recall fi	ve differenc	es between o	om and exe f	ile.		(05 Marks)
				<i>y</i>	Modella			
3	a.	Evnlain	the followin	g instruction	Module-	<u>Z</u>		
5	α.	(i)	DAA	ginisuucuon		*	7-7	
		(ii)	RCR				*	
		\ /	CLC			4		(05 Marks)
	b.		A	h algorithm	to calculate t	he sum of two l	oytes of data (13h	
						dress 0020h in		(07 Marks)
	c.						ulate the total sun	
		data. Th	e decimal d	ata are 125,	235, 197, 91,	95 and store the	he result in a men	
	-	, Y	4		Z.	7		(08 Marks)
		Ç			4 OR			
4	a.	Explain	the followin	g instruction				
		(i)	SBB	5 monachen				
		(ii)	RCL					
		(iii)	SHL					(03 Marks)
	b.	Write an	(ALP) prog	gram to:				,
		(i)	Convert A	ASCII numbe	er '23' to pac	ked BCD.		
		(ii)	Add the I	BCD number	23 with 45.			
		(iii)		he result of t	he above add	ition to ASCII.		(08 Marks)
	c.		ALP to:	<b>y</b>				
		(i)	To clear t					
		(ii)	A		nter of the sci		ď.	
		(iii)	I o displa	y a string "V	TO WEL CO	MES YOU" on	the screen.	(09 Marks)
			77					

(10 Marks)

(10 Marks)

## Module-3 Explain the following instruction with syntax: (i) **IMUL** (ii) CLD (iii) **STOS** (iv) **SCASB** (06 Marks) b. Write an ALP to check whether given string is a palindrome or not, if so display the respective message. c. Find the control word of 8255 if PA = in PB = out PCO-PC3 = in PC4-PC7 = out and write the program using the above control word, to get the data from port A and send it to port B. Use port adders of 300-303H for 8255 chip. (07 Marks) Explain the following instruction with syntax: (i) **IDiV** (ii) STD (iii) OUT LoDSB (iv) (06 Marks) b. Write an ALP to check whether two strings STR1 and STR2 equal or not. If equal display "STRINGS ARE EQUAL" else display "STRINGS ARE NOT EQUAL". (07 Marks) c. Design a memory interface which uses 8 numbers of 2764 EPROM chip for 64\*8 memory, for the adders range between F0000 to FFFFF. (07 Marks) Module-4 7 With neat diagram, explain ARM core data flow model. (07 Marks) With neat diagram, summarize the complete set of registers of ARM. (07 Marks) With neat diagram, outline the various functional blocks of embedded system. (06 Marks) OR Compare the differences between RISC and CISC design phylosophy. (06 Marks) With neat diagram, explain the various fields of CPSR. (07 Marks) With neat diagram, show how pipelining mechanism used in RISC processors. (07 Marks) Module-5 a. Explain with example Branch instructions of ARM. (10 Marks) b. Explain with example logical and data processing instruction of ARM. (10 Marks)

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OR

With neat diagram, explain the function of Barrel shifter in ARM.

Explain with example, compare and multiply instructions of ARM.