

CBCS SCHEME

18CS44

## Fourth Semester B.E. Degree Examination, Dec.2023/Jan.2024 Microcontroller and Embedded Systems

Time: 3 hrs.

Max. Marks: 100

Note: Answer any FIVE full questions, choosing ONE full question from each module.

Module-1

1 Explain ARM core data flow model with neat diagram. (08 Marks) List and explain ARM processor modes. Also explain ARM core changing from user mode

to interrupt request mode on an exception, with a neat diagram.

c. Differentiate: Microprocessor vs Microcontroller.

(08 Marks) (04 Marks)

2 What is pipeline? Compare ARM7 three stage pipeline, ARM9 fire-stage pipeline and ARM10 six stage pipeline. (08 Marks)

b. Explain exception of interrupt. Narrate interrupt vector table.

(08 Marks)

Write a short note on Hardware extensions for a ARM core.

(04 Marks)

Module-2

Explain single register load store addressing modes with examples. (08 Marks)

Explain the program status register instructions. Also write a code fragment to.

Copy the cpsr into register rl i)

Clear bit 7 of rl ii)

Copy the register r2 back to cpsr.

(08 Marks)

Explain the following ARM instructions with examples:

ii) MRS iii) STMIB iv) SWP. (04 Marks)

OR

With neat diagram and example, explain block memory transfer in the memory map using load-store multiple instructions. (08 Marks)

b. Explain stack operation of ARM processors. Also explain the load-store multiple addressing aliases available to support stack operations. (08 Marks)

Explain software interrupt instruction with its syntax.

(04 Marks)

Module-3

Write a function in assembly that can sum any number of integers. The argument should be 5 the number of integers to sum followed by a list of the integers.

b. What is an Embedded system? Explain the different classifications of embedded systems. Give example for each. (08 Marks)

Write the difference between microprocessors and microcontrollers.

(04 Marks)

OR

What is Programmable Logic Device (PLD)? What are the different types of PLDs? Explain advantages of PLDs in embedded system design.

b. What is 7-segment LED display? What are two different configurations of 7-segment LED display? Explain. (08 Marks)

Differentiate sensors v/s actuators.

(04 Marks)

Module-4 What is hardware software co-design? Explain the fundamental issues in hardware software (08 Marks) co-design. Explain the product life-cycle curve of an embedded product development. (08 Marks) What is the difference between compiler and cross compiler? (04 Marks) Explain different embedded firmwave design approaches in detail. (08 Marks) 8 a. (08 Marks) Explain sequential program model. (04 Marks) Differentiate 'C' versus 'Embedded C'. Module-5 What is Kernal? What are the different functions handled by Kernal for a general purpose 9 (08 Marks) What is Task Control Block (TCB)? Explain structure of TCB. (08 Marks) b. Differentiate between thread and process. (04 Marks) OR Explain different techniques available for embedding firmwave into the target board for a 10 (08 Marks) non-os based embedded system. b. Explain structure of a process and explain process life cycle with various activities involved (08 Marks) in the creation of process. Write a note on Remote Procedure Call (RPC) mechanism for IPC. (04 Marks)