



- 6 a. Explain how the 8086 processor finds the address of interrupt service subroutine for particular interrupt. (04 Marks)  
b. Explain Interrupt system of 8086 processor. Write the sequence of events takes place when an interrupt occurs. (08 Marks)  
c. What is meant by Modular Programming? Also write a procedure to generate a delay of 2 msec, for the 8086 operated at 5 MHz. (08 Marks)
- 7 a. Compare memory mapped I/O and I/O mapped I/O interfacing schemes. (04 Marks)  
b. Why the address demultiplexing is required in 8086 processor? Explain how it is done for minimum mode of operation. (08 Marks)  
c. What is Wait State? How do you introduce it? Explain with necessary timing diagram with respect to 8086 processor. (08 Marks)
- 8 a. Explain the function of following 8086 pins :  
i)  $\overline{\text{BHE}}$  ii) ALE iii) INTR iv)  $\text{DT}/\overline{\text{R}}$ . (04 Marks)  
b. Sketch memory read bus cycle of 8086 and explain. (08 Marks)  
c. Explain 8255 modes of operations. (08 Marks)
- 9 a. Write 8255 control word to set  $\text{PC}_5$ . (04 Marks)  
b. Interface a stepper motor to 8086 processor using 8255 and write an ALP to it for  $180^\circ$  in clock wise direction. (08 Marks)  
c. Explain Mode - 0 and Mode - 3 operations of 8254. (08 Marks)
- 10 a. Bring out the differences between CICS and RISC processors. (04 Marks)  
b. Describe any five DOS functions related with INT21H. (08 Marks)  
c. Using DOS functions write an 8086 ALP to read a two digit hexadecimal number and display the same on the console. (08 Marks)

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