Time: 3 hrs.

Second Semester MCA Degree Examination, Dec.2015/Jan.2016

System Programming/System Software Max. Marks: 100

Note: Answer any FIVE full questions. (10 Marks) Explain SIC/XE architecture. Explain the following features of SIC machine (06 Marks) iii) Data Format. ii) Instruction Set i) Registers Calculate Target Address for the following: (x) = 003000(Pc) = 000003nixbpe Disp/address Opcode 0000 0000 0000 1 1 1 0 1 0 000100 i) 00001100 0011 0000 0011 1 1 0 0 0 1 000000 ii) (04 Marks) Write the algorithm for pass 2 of two - pass assembler and explain the functions of it. (10 Marks) (10 Marks) Explain MASM assembler. b. Write an algorithm for Pass 1 of a linking loader and explain. (10 Marks) 3 a. (04 Marks) Explain Absolute loader. b. Explain how relocation is done using (06 Marks) ii) Bit masks i) Modification record (07 Marks) Explain MS - DOS linker. What is Dynamic linking? Explain with a neat diagram loading and calling of a subroutine using dynamic linking. (05 Marks) Give a brief on Automatic library search.

- Explain the components of editor and its functions with a neat diagram. (10 Marks) Describe the debugging functions and capabilities of an interactive debugging system. 5 a.

(10 Marks)

- Explain the following:
 - (04 Marks) i) Macro definition and Macro Expansion (04 Marks)
 - ii) Generation of Unique labels.
 - Write an algorithm for one pass macro processor and discuss the different data structures used by the macro processor.
 - (07 Marks) Discuss in detail about Recursive macro expansion. a.
 - (08 Marks) Explain ANSI C Macro processor.
 - b. (05 Marks) Discuss in detail about General Purpose Macro processors.
- Explain Machine dependent code optimization of compiler with an example. (10 Marks) 8 a.
- Write a note on the following: b. (10 Marks) ii) P – code compilers. i) Interpreters