## CBCS SCHEME

USN						15CS72
USIN						

## Seventh Semester B.E. Degree Examination, Dec.2018/Jan.2019 **Advanced Computer Architecture**

Time: 3 hrs. Max. Marks: 80 Note: Answer any FIVE full questions, choosing ONE full question from each module. Module-1

a. List the performance factors and system attributes. Explain how performance factors are influenced by system attributes. Explain the architecture of vector super computer with neat diagram. (08 Marks) What are the conditions of parallelism? Explain the types of data dependence. (06 Marks) What are the metrics affecting scalability of a computer system? (06 Marks) What are the important characteristics of parallel algorithms? (04 Marks) Module-2 What are the characteristic of CISC and RISC architecture? (04 Marks) What are the virtual memory models for multiprocessor system? (04 Marks) Explain address translation mechanism using TLB and page table. (08 Marks) Explain typical superscalar RISC processor architecture. (08 Marks) b. Explain inclusion, coherence and locality properties. (08 Marks) Module-3 What is arbitration? Explain different types of arbitration. (08 Marks) b. Explain sequential and weak consistency models. (08 Marks) OR What are the different techniques for branch prediction? Explain. (08 Marks) b. Explain multiply pipeline design to multiply two 8-bit integers. (08 Marks) Module-4 Explain routing in omega network. b. What are different vector – access memory schemes? Explain any two of them.

(08 Marks) (08 Marks) What are the implementation models of SIMD? Explain them. (08 Marks) Explain four context-switching policies. (08 Marks) Module-5

What are the issues in using shared-variable model? 9 (08 Marks) Explain different phases of parallelizing compiler with a diagram. (08 Marks) Explain testing algorithm for dependence testing. 10 (08 Marks)

What are the principles of synchronization mechanisms? Explain them. (08 Marks)